

Configuration SRAM for FBGA

Manufacturer:	Intel Corp
Package/Case:	PLCC
Product Type:	Programmable Logic ICs
Lifecycle:	Obsolete



Images are for reference only

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General Description

The MAX 9000 family of in-system-programmable, high-density, highperformance EPLDs is based on Altera’s third-generation MAX architecture. Fabricated on an advanced CMOS technology, the EEPROMbased MAX 9000 family provides 6,000 to 12,000 usable gates, pin-to-pin delays as fast as 10 ns, and counter speeds of up to 144 MHz.

Recommended For You

EPM3256AQC208-10N	EPCQ32ASI8N	EPCQ32SI8N
Intel Corp	Intel Corp	Intel Corp
QFP208	SOP8	SOP8
EPCQ64ASI16N	EPCQ16SI8N	EPC2II32
Intel Corp	Intel Corp	Intel Corp
SOP16	SOP8	QFP
EPM7128STC100-15N	EP1C6Q240I7N	EPCQ128SI16N
Intel Corp	Intel Corp	Intel Corp
QFP100	QFP240	SOP16

EPM7128SLC84-15N

Intel Corp

PLCC

EPC1213PC8

Intel Corp

DIP8

EP1K30TC144-3N

Intel Corp

QFP

EPCS1S18

Intel Corp

SOP-8

EPC1P18N

Intel Corp

DIP8

EPC2LI20N

Intel Corp

PLCC