

FPGA FLEX 6000 Family 16K Gates 1320 Cells 125MHz 0.42um  
Technology 5V 208-Pin PQFP



Images are for reference only

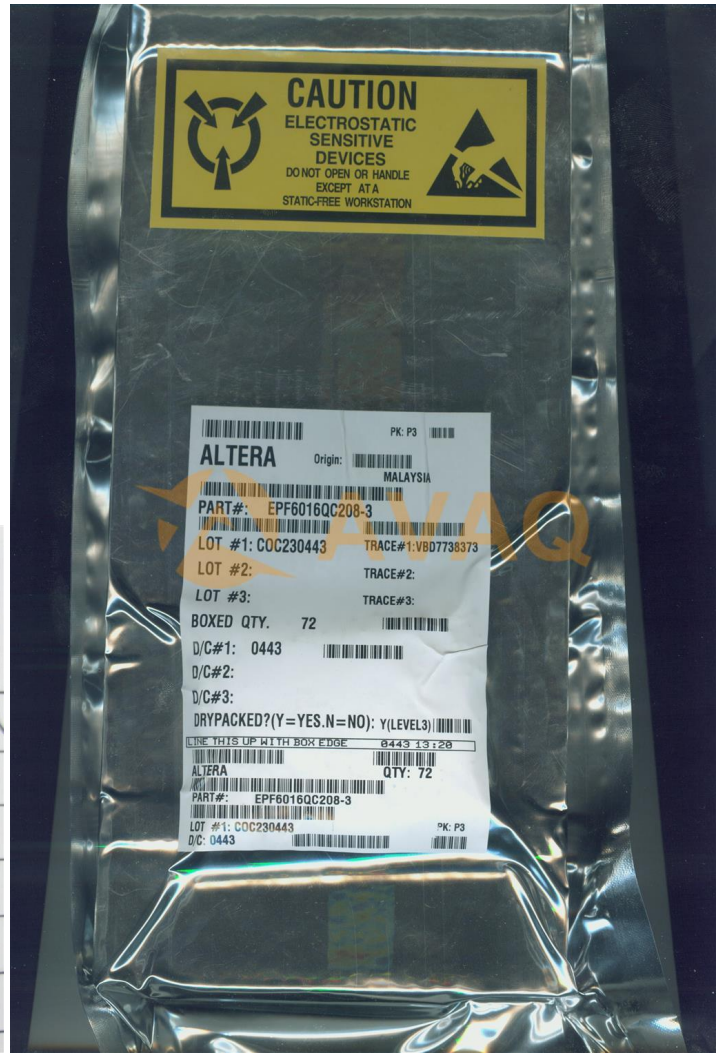
<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

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## General Description

The Altera® FLEX 6000 programmable logic device (PLD) family provides a low-cost alternative to high-volume gate array designs. FLEX 6000 devices are based on the OptiFLEX architecture, which minimizes die size while maintaining high performance and routability. The devices have reconfigurable SRAM elements, which give designers the flexibility to quickly change their designs during prototyping and design testing. Designers can also change functionality during operation via in-circuit reconfiguration.

FLEX 6000 devices are reprogrammable, and they are 100% tested prior to shipment. As a result, designers are not required to generate test vectors for fault coverage purposes, allowing them to focus on simulation and design verification. In addition, the designer does not need to manage inventories of different gate array designs. FLEX 6000 devices are configured on the board for the specific functionality required.



## Recommended For You

### EPMB256AQC208-10N

Intel Corp

QFP208

### EPCQ32ASI8N

Intel Corp

SOP8

### EPCQ32SI8N

Intel Corp

SOP8

### EPCQ64ASI16N

Intel Corp

SOP16

### EPCQ16SI8N

Intel Corp

SOP8

### EPC21132

Intel Corp

QFP

### EPM7128STC100-15N

Intel Corp

QFP100

### EP1C6Q240I7N

Intel Corp

QFP240

### EPCQ128SI16N

Intel Corp

SOP16

**EPM7128SLC84-15N**

Intel Corp

PLCC

**EPC1213PC8**

Intel Corp

DIP8

**EP1K30TC144-3N**

Intel Corp

QFP

**EPCS1S18**

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SOP-8

**EPC1PI8N**

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