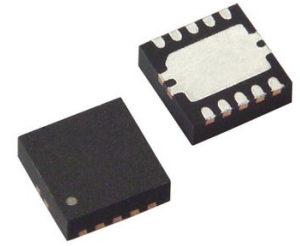


Clock Generator 0.001MHz to 750MHz-IN 2075MHz-OUT 64-Pin WQFN EP T/R



Images are for reference only

Manufacturer: [Texas Instruments, Inc](#)

Package/Case: WQFN

Product Type: Clock & Timer ICs

RoHS: RoHS Compliant/Lead free 

Lifecycle: Active

[Inquiry](#)

General Description

The LMK0482x family is the industry's highest performance clock conditioner with JEDEC JESD204B support.

The 14 clock outputs from PLL2 can be configured to drive seven JESD204B converters or other logic devices using device and SYSREF clocks. SYSREF can be provided using both DC and AC coupling. Not limited to JESD204B applications, each of the 14 outputs can be individually configured as high performance outputs for traditional clocking systems.

The high performance combined with features like the ability to trade off between power or performance, dual VCOs, dynamic digital delay, holdover, and glitchless analog delay make the LMK0482x family ideal for providing flexible high performance clocking trees.

Key Features

JEDEC JESD204B Support

Ultra-Low RMS Jitter
88 fs RMS Jitter (12 kHz to 20 MHz)

91 fs RMS Jitter (100 Hz to 20 MHz)

162.5 dBc/Hz Noise Floor at 245.76 MHz

Up to 14 Differential Device Clocks from PLL2
Up to 7 SYSREF Clocks

Maximum Clock Output Frequency 3.1 GHz

LVPECL, LVDS, HSDS, LCPECL
Programmable Outputs from PLL2

Up to 1 Buffered VCXO/Crystal Output from PLL1
LVPECL, LVDS, 2xLVCMOS Programmable

Dual Loop PLLatinum™ PLL Architecture

PLL1
Up to 3 Redundant Input Clocks
Automatic and Manual Switch-Over Modes

Hitless Switching and LOS

Integrated Low-Noise Crystal Oscillator Circuit

Holdover mode when Input Clocks are Lost

PLL2
Normalized [1 Hz] PLL Noise Floor of
227 dBc/Hz

Phase Detector Rate up to 155 MHz

OS_{Cin} Frequency-Doubler

Two Integrated Low-Noise VCOs

50% Duty Cycle Output Divides, 1 to 32
(even and odd)

Precision Digital Delay, Dynamically Adjustable

25 ps Step Analog Delay

Multi-mode: Dual PLL, single PLL, and Clock
Distribution

Industrial Temperature Range: 40 to 85°C

Supports 105°C PCB Temperature (Measured at Thermal Pad)

3.15-V to 3.45-V Operation

Package: 64-pin QFN (9.0 × 9.0 × 0.8 mm)

Recommended For You

LMK00334RTVR

Texas Instruments, Inc
WQFN32

LMC555CM

Texas Instruments, Inc
SOP8

LM555CM

Texas Instruments, Inc
SOP8

LMC555CMX/NOPB

Texas Instruments, Inc
SOP8

LM555CN

Texas Instruments, Inc
DIP8

LM555J/883

Texas Instruments, Inc
CDIP8

LMC555CMMX

Texas Instruments, Inc
MSOP8

LM555CN/NOPB

Texas Instruments, Inc
DIP8

LMC555CMMX/NOPB

Texas Instruments, Inc
VSSOP8

LMK00101SQE/NOPB

Texas Instruments, Inc
WQFN32

LM555H/883

Texas Instruments, Inc
CAN

LMK1C1102DQFR

Texas Instruments, Inc
WSO8-8

LMC555CN

Texas Instruments, Inc
DIP

LMC555IMX/NOPB

Texas Instruments, Inc
SOP8

LMC555CIP

Texas Instruments, Inc
DSBGA