

**FPGA Cyclone® Family 5980 Cells 320.1MHz 130nm Technology  
1.5V 240-Pin PQFP**

<b>Manufacturer:</b>	Intel Corp
<b>Package/Case:</b>	QFP240
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete



Images are for reference only

Inquiry

### General Description

The Cyclone® field programmable gate array family is based on a 1.5-V, 0.13-µm, all-layer copper SRAM process, with densities up to 20,060 logic elements (LEs) and up to 288 Kbits of RAM. With features like phase-locked loops (PLLs) for clocking and a dedicated double data rate (DDR) interface to meet DDR SDRAM and fast cycle RAM (FCRAM) memory requirements, Cyclone devices are a cost-effective solution for data-path applications. Cyclone devices support various I/O standards, including LVDS at data rates up to 640 megabits per second (Mbps), and 66- and 33-MHz, 64- and 32-bit peripheral component interconnect (PCI), for interfacing with and supporting ASSP and ASIC devices. Altera also offers new low-cost serial configuration devices to configure Cyclone devices.



### Recommended For You

**EPMB256AQC208-10N**

Intel Corp

QFP208

**EPCQ32ASI8N**

Intel Corp

SOP8

**EPCQ32SI8N**

Intel Corp

SOP8

**EPCQ64ASI16N**

Intel Corp

SOP16

**EPCQ16SI8N**

Intel Corp

SOP8

**EPC21I32**

Intel Corp

QFP

**EPM7128STC100-15N**

Intel Corp

QFP100

**EP1C6Q240I7N**

Intel Corp

QFP240

**EPCQ128SI16N**

Intel Corp

SOP16

**EPM7128SLC84-15N**

Intel Corp

PLCC

**EPC1213PC8**

Intel Corp

DIP8

**EP1K30TC144-3N**

Intel Corp

QFP

**EPCS1SI8**

Intel Corp

SOP-8

**EPC1PI8N**

Intel Corp

DIP8

**EPC2LI20N**

Intel Corp

PLCC