

## PALCE22V10H-10JC/5

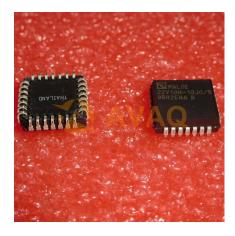
## SPLD PAL Family 10 Macro Cells 125MHz 5V 28-Pin PLCC

Manufacturer: <u>Lattice Semiconductor Corp</u>

Package/Case: PLCC28

**Product Type:** Programmable Logic ICs

Lifecycle: Obsolete



Images are for reference only

Inquiry

## **General Description**

The PALCE22V10 provides user-programmable logic for replacing conventional SSI/MSI gates and flip-flops at a reduced chip count.

The PALCE22V10Z is an advanced PAL® device built with zero-power, high-speed, electrically erasable CMOS technology. It provides user-programmable logic for replacing conventional zeropower CMOS SSI/MSI gates and flip-flops at a reduced chip count.

The PALCE22V10Z provides zero standby power and high speed. At 30  $\mu$ A maximum standby current, the PALCE22V10Z allows battery-powered operation for an extended period.

The PAL device implements the familiar Boolean logic transfer function, the sum of products. The PAL device is a programmable AND array driving a fixed OR array. The AND array is programmed to create custom product terms, while the OR array sums selected terms at the outputs.

The product terms are connected to the fixed OR array with a varied distribution from 8 to 16 across the outputs (see Block Diagram). The OR sum of the products feeds the output macrocell. Each macrocell can be programmed as registered or combinatorial, and active-high or active low. The output configuration is determined by two bits controlling two multiplexers in each macrocell.



## **Recommended For You**

PALCE22V10Q-25PC/4

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DIP

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