


## SLC NAND Flash Parallel 3.3V 4G-bit 512M x 8 48-Pin TSOP-I Tray

<b>Manufacturer:</b>	<a href="#">Micron Semiconductor Products Inc</a>
<b>Package/Case:</b>	TSOP48
<b>Product Type:</b>	Memory
<b>RoHS:</b>	RoHS Compliant/Lead free 
<b>Lifecycle:</b>	Active



Images are for reference only

[Inquiry](#)

### General Description

Micron NAND Flash devices include an asynchronous data interface for high-performance I/O operations. These devices use a highly multiplexed 8-bit bus (I/Ox) to transfer commands, address, and data. There are five control signals used to implement the asynchronous data interface: CE#, CLE, ALE, WE#, and RE#. Additional signals control hardware write protection and monitor device status (R/B#).

This hardware interface creates a low pin-count device with a standard pinout that remains the same from one density to another, enabling future upgrades to higher densities with no board redesign.

A target is the unit of memory accessed by a chip enable signal. A target contains one or more NAND Flash die. A NAND Flash die is the minimum unit that can independently execute commands and report status. A NAND Flash die, in the ONFI specification, is referred to as a logical unit (LUN). There is at least one NAND Flash die per chip enable signal. For further details, see Device and Array Organization.

This device has an internal 4-bit ECC that can be enabled using the GET/SET features.

See Internal ECC and Spare Area Mapping for ECC for more information.

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BGA

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Micron Semiconductor Products Inc  
WPDFN8

#### [MI41K128MI6JT-125 XIT:K](#)

Micron Semiconductor Products Inc  
FBGA

#### [MT29F1G08ABAEAWP:E](#)

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TSOP48

#### [MI47H128M8SH-25E:M](#)

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FBGA

#### [MI29F32G08ABAAAWP-ITZ:A](#)

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**MI47H64MI6NF-25E:M**

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**MI41K64MI6TW-107:J**

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**MI41K256MI6TW-107 AIT:P**

Micron Semiconductor Products Inc  
FBGA96

**MI41K128MI6JT-125:K**

Micron Semiconductor Products Inc  
FBGA

**MI47H128MI6RT-25E:C**

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**MI48LC16MI6A2P-6A:G**

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**MI29F4G08ABADAWP-IT:D**

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SOP16