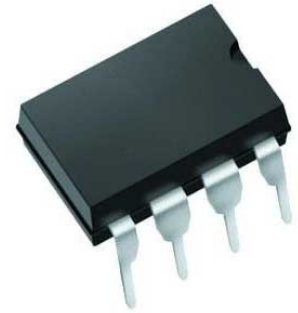


## PROM Serial 243.3K-bit 3.3V 8-Pin PDIP



Images are for reference only

[Inquiry](#)

**Manufacturer:** [AMD Xilinx, Inc](#)

**Package/Case:** DIP8

**Product Type:** Memory

**RoHS:** RoHS Compliant/Lead free 

**Lifecycle:** Obsolete

### General Description

The Spartan family of PROMs provides an easy-to-use, cost-effective method for storing Spartan device configuration bitstreams.

When the Spartan device is in Master Serial mode, it generates a configuration clock that drives the Spartan FPGA PROM. A short access time after the rising clock edge, data appears on the PROM DATA output pin that is connected to the Spartan device DIN pin. The Spartan device generates the appropriate number of clock pulses to complete the configuration. Once configured, it disables the PROM. When a Spartan device is in Slave Serial mode, the PROM and the Spartan device must both be clocked by an incoming signal.

For device programming, either the Xilinx Alliance or the Foundation series development systems compiles the Spartan device design file into a standard HEX format which is then transferred to most commercial PROM programmers.

### Recommended For You

#### XC17S20XLPD8C

AMD Xilinx, Inc

DIP8

#### XC17S30XLPD8I

AMD Xilinx, Inc

DIP8

#### XC17S40XLPD8C

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#### XC17S05PD8I

AMD Xilinx, Inc

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#### XC17S100APD8C

AMD Xilinx, Inc

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#### XC17S40PD8C

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DIP8

#### XC17S30XLPD8C

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#### XC17V16VQ44I

AMD Xilinx, Inc

VQFP44

#### XC17S50ASO20C

AMD Xilinx, Inc

SOP-20L

**XC17S30XLVOG8I**

AMD Xilinx, Inc

8-TSOP

**XC17S40SO20I**

AMD Xilinx, Inc

20-SOIC

**XC17S20VO8I**

AMD Xilinx, Inc

8-TSOP

**XC17S10XLVO8I**

AMD Xilinx, Inc

8-TSOP

**XC17S20XLVO8C**

AMD Xilinx, Inc

8-TSOP

**XC17S40XLPD8I**

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