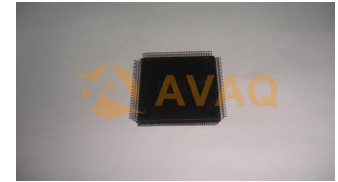


**CPLD MAX® 7000S Family 2.5K Gates 128 Macro Cells 100MHz  
5V 160-Pin PQFP Tray**



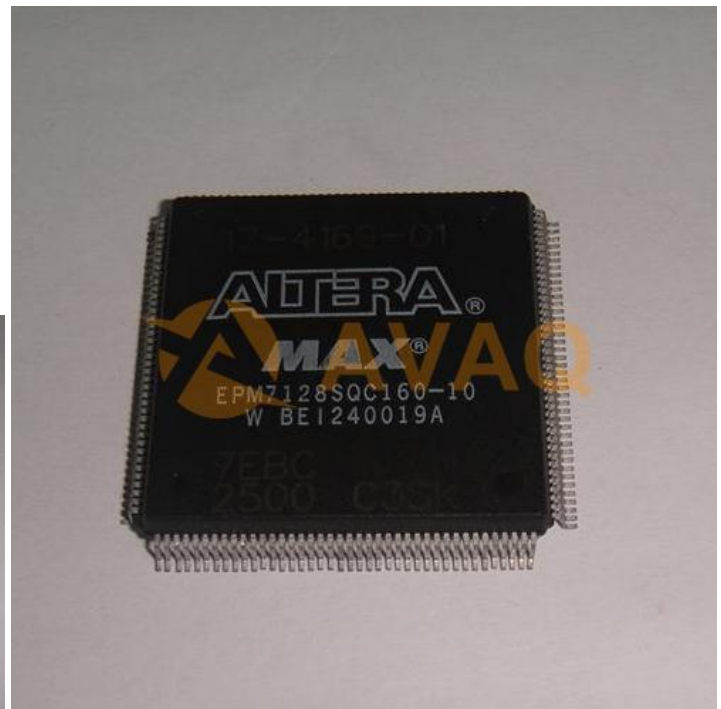
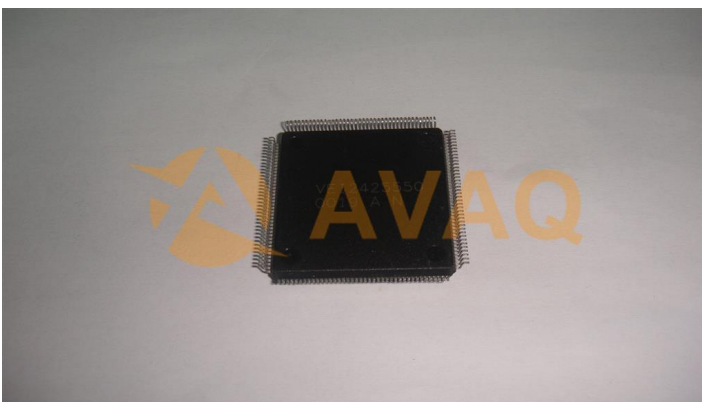
Images are for reference only

[Inquiry](#)

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

### General Description

The MAX 7000 family of high-density, high-performance PLDs is based on Altera's second-generation MAX architecture. Fabricated with advanced CMOS technology, the EEPROM-based MAX 7000 family provides 600 to 5,000 usable gates, ISP, pin-to-pin delays as fast as 5 ns, and counter speeds of up to 175.4 MHz. MAX 7000S devices in the -5, -6, -7, and -10 speed grades as well as MAX 7000 and MAX 7000E devices in -5, -6, -7, -10P, and -12P speed grades comply with the PCI Special Interest



### Recommended For You

#### [EPM7128SQC100-15](#)

Intel Corp

QFP

#### [EPM7128SLC84-10](#)

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PLCC84

#### [EPM7128SLC84-15](#)

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**EPM7128SQC100-7**

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**EPM7128EQC100-10**

Intel Corp

QFP

**EPM7128STC100-15**

Intel Corp

TQFP

**EPM7128EQC160-10**

Intel Corp

QFP160

**EPM7128EQC100-7**

Intel Corp

QFP

**EPM7064QC100-12**

Intel Corp

QFP

**EPM7128EQC160-12**

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QFP

**EPM7064QC100-7**

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QFP

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PQFP-100