
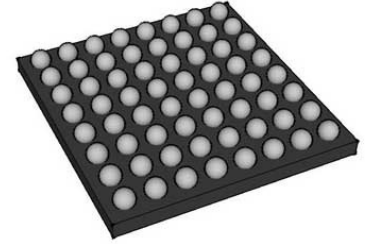


DSP Floating-Point 32bit/40bit 333MHz 256-Pin SBGA Tray

Manufacturer:	Analog Devices, Inc
Package/Case:	BGA
Product Type:	Embedded Processors & Controllers
RoHS:	RoHS Compliant/Lead free 
Lifecycle:	Active



Images are for reference only

[Inquiry](#)

General Description

The third generation of SHARC® Processors offers increased performance, audio and application-focused peripherals, and new memory configurations. The ADSP-21369 increases performance to 400MHz while simplifying algorithm development with the integration of a high-bandwidth and very flexible external memory interface. The ADSP-21369 is based on a single-instruction, multiple-data (SIMD) core which supports both 32-bit fixed-point and 32-/40-bit floating point arithmetic formats and is completely code-compatible with all prior SHARC Processors allowing for maximum reuse of legacy code.

The ADSP-21369 increases the amount of on-chip memory to 2Mb SRAM and 6Mb ROM while integrating a variety of audio-specific and general purpose peripherals. Peripherals such as an all digital S/PDIF transmitter/receiver, 8-channel asynchronous sample rate converter, 8 high-speed serial ports, 4 precision clock generators, and multiple serial interfaces combine to ensure the ADSP-21369 maximizes system throughput while minimizing system bill of materials costs.

Key Features

400MHz SIMD SHARC Core, capable of 2.4 GFLOPS peak performance

2Mbits SRAM; 6 Mbits customer-definable ROM

High bandwidth, 32-bit external memory interface supporting glue-less interface to SDRAM, SRAM, and FLASH

Digital Audio Interface (DAI) enabling user-definable access to system peripherals including 8 serial ports (SPORTs), S/PDIF Tx/Rx, 8-channel asynchronous sample rate converter, and 4 precision clock generators.

Digital Peripheral Interface (DPI) enabling user-definable access to system peripherals including 2 SPI-compatible ports, 2 UARTs, 3 full-featured timers, and a two-wire interface compliant to the I2C standard.

34 zero-overhead DMA channels

16 Pulse Width Modulation (PWM) channels

208-lead LQFP-EP and 256-ball SBGA packages

Commercial and industrial temperature ranges

Maximum core performance is reduced for LQFP package

Recommended For You

ADSP-BF592KCPZ

Analog Devices, Inc
LFCSP64

ADSP-2183KSTZ-210

Analog Devices, Inc
QFP100

ADSP-BF534BBCZ-4B

Analog Devices, Inc
BGA

ADSP-BF537BBCZ-5A

Analog Devices, Inc
CSPBGA-182

ADSP-BF532SBSTZ400

Analog Devices, Inc
LQFP176

ADSP-BF533SBBCZ500

Analog Devices, Inc
BGA

ADSP-BF533SKBCZ-6V

Analog Devices, Inc
BGA

ADSP-BF533SBBZ500

Analog Devices, Inc
BGA

ADSP-BF533SBSTZ400

Analog Devices, Inc
QFP

ADSP-BF533SBBCZ400

Analog Devices, Inc
BGA160

ADSP-2171BSTZ-133

Analog Devices, Inc
QFP

ADSP-2186MKST-300

Analog Devices, Inc
QFP

ADSP-BF512BSWZ-4

Analog Devices, Inc
QFP176

AD1940YST

Analog Devices, Inc
QFP48

ADSP-BF534BBCZ-5B

Analog Devices, Inc
BGA